

Application/Contral: 09/980,873

Applicant(s)/Patent Under Reexamination FUKUNAKA, TOSHIAKI

Examiner

Scott R. Wilson

Art Unit 2826

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-4978938 A	12-1990	Partin et al.	338/32 R
	В	US-			
	С	US-			
	D	US-			
	Е	US-			
	F	· US-			
	G	US-			
	Н	US-			
	1	US-			·
	j	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP-2000012919-A	01-2000	Japan	Fukunaka et al.	H01L 43/04
	0	JP-10227845-A	08-1998	Japan	Hattori et al.	H01L 43/02
	Ь					
	Q					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	IBM Technical Disclosure Bulletin, November 1972. Vol. 15, Issue 6, pages 1977-1980 "Packaging of Integrated Circuits"					
	v						
	w						
	х						

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.